



Sheet 1 of 1

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE					ATTY. DOCKET NO. <b>RCA 88318</b>		SERIAL NO. <b>09/ 614,632</b>
<b>INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97</b>					APPLICANT <b>Wai-Man Lam et al.</b>		
(Use several sheets if necessary)					FILING DATE <b>July 12, 2000</b>		GROUP <b>2613</b>
<b>U. S. PATENT DOCUMENTS</b>							
EXAMINE INITIAL		DOCUMENT NUMBER	ISSUE DATE	APPLICANT/PATENTEE	CLASS	SUB-CLASS	FILING DATE IF APPROPRIATE
<i>AA</i>	AA	5,410,354	04/25/95	K. M. Uz	348	426	
<i>AC</i>	AB	5,844,636	12/01/98	K. Joseph et al.	348	845	
<i>AC</i>	AC	5,742,343	04/21/98	B. G. Haskell et al.	348	415	
	AD						
	AE			*EUROPEAN SEARCH REPORT DATED 10 MAY 2004 ATTACHED*	<b>RECEIVED</b>		
	AF				JUN 15 2004		
	AG				Technology Center 2600		
	AH						
	AI						
<b>FOREIGN PATENT DOCUMENTS</b>							
		DOCUMENT NUMBER	PUBL. DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION Yes      No
	AL						
	AM						
	AN						
	AO						
	AP						
	AQ						
<b>OTHER INFORMATION (Including Author, Title, Pub Date, Pertinent Pages, Country, Etc.)</b>							
<i>AA</i>	AR	Okubo, S. et al.: "MPEG-2 requirements, profiles and performance verification - Framework for developing a generic video coding standard", SIGNAL PROCESSING. IMAGE COMMUNICATION, ELSEVIER SCIENCE PUBLISHERS, AMSTERDAM, NL, vol. 7, no. 3, 1 Sept. 1995, Pages 201-209, XP004047103.					
<i>AC</i>	AS	Takahashi, H. et al.: "SINGLE-CHIP MPEG2 MP HL DECODER WITH MULTI-DECODE AND SEAMLESS DISPLAY FEATURES", FUJITSU-SCIENTIFIC AND TECHNICAL JOURNAL, FUJITSU LIMITED, KAWASAKI, JP, vol. 36, no. 1, June 2000, pages 48-55, XP001108192.					
<i>AC</i>	AT	Suguri, K. et al., "A SCALABLE ARCHITECTURE OF REAL-TIME MP@HL MPEG-2 VIDEO ENCODER FOR MULTI-RESOLUTION VIDEO", vol. 3653, 27 January 1999, pages 895-904, XP0001189206.					
EXAMINER <i>Guy H. Eriksen</i>				DATE CONSIDERED <i>9/16/04</i>			
SUBMITTED BY: <b>GUY H. ERIKSEN</b>				REG. NO. : <i>Guy H. Eriksen</i>	DATE: <b>June 7, 2004</b>		

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